

# CS314 - Reductions

Note Title

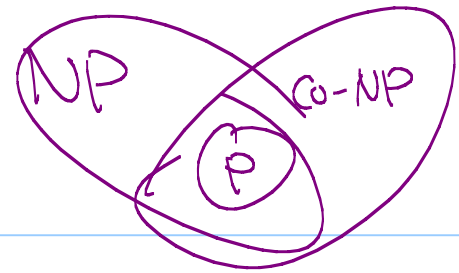
3/24/2010

## Announcements

- Next HW up today / tomorrow  
written, due ~~next Friday~~ at beginning of class  
Sometime

(probably Easter Wed.)

## P, NP, & co-NP



Consider decision problems - output is a single boolean (yes or no).

Define:

- P: the set of problems that can be solved in polynomial time

• NP: the set of decision problems where if the answer is Yes, there is a proof of this that can be checked in polynomial time

*non-deterministic polynomial* →

- co-NP: If answer is No, that can be checked in polynomial time.

## NP-Hard

Def: A problem  $\Pi$  is NP-Hard

$\iff$  (if + only if)

if  $\Pi$  can be solved in polynomial time, then  $P = NP$ .

Def: A problem is NP-Complete if it is both NP-Hard and in NP. ]

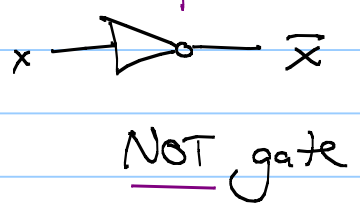
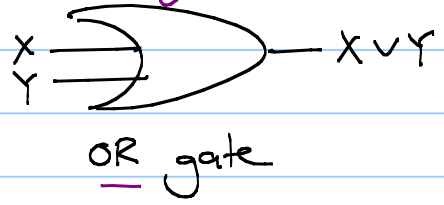
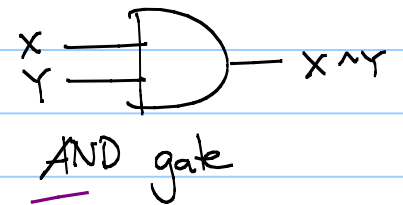
These are the "hardest" problems in NP.

# Circuit-SAT

Input: boolean circuit, with T/F inputs

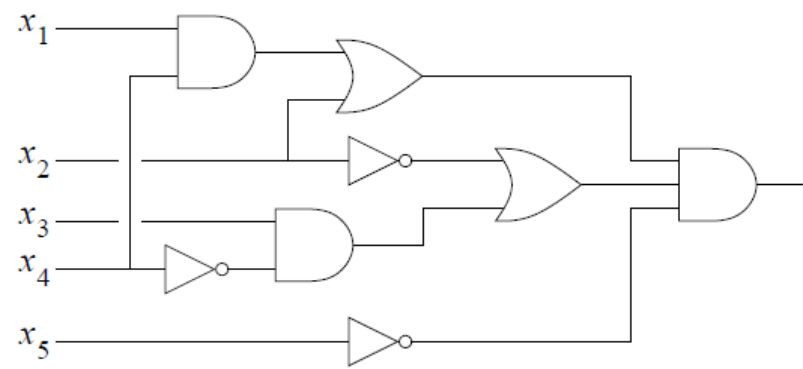
Output: T or F

Q: Is there a set of assignments to the inputs so that output=T?



Ex:

5 inputs



1 output

A boolean circuit. inputs enter from the left, and the output leaves to the right.

## Cook-Levin Theorem:

Circuit-satisfiability is NP-Complete.

### Comments:

The proof is amazing - takes any NP problem & changes it into a circuit with polynomial size, so that:

true answer for NP problem exists



the resulting circuit is satisfiable

However:

This is pretty much the only direct proof to ever show a problem is NP-Complete!

So how do we say other problems are just as hard?

# Reductions

Dfn:  $Y \leq_p X$  (read  $Y$  is polynomial time reducible to  $X$ )

if  $Y$  can be solved using a polynomial number of steps plus a polynomial number of calls to an algorithm (or "black box") that solves  $X$ .

Ex: test question about near trees!

Found cycles

Ex: sorting

Thm: Supps  $Y \leq_p X$ . If  $X$  can be solved in polynomial time, then so can  $Y$ .

pf:

Replace "black box" to solve  $X$  with code that runs in poly. time.

Solving  $Y$  now takes  
 $\cup$  poly time + (poly # of calls to  $X$ ) \* (running time for  $X$ )  
 $=$  poly. time





This is useful for algorithms!

But what if we don't know of a polynomial time algorithm?

NP-Complete?

$$P \rightarrow Q \\ \Downarrow \\ (\neg Q) \rightarrow \neg P \text{ (contrapositive)}$$

[ Spps  $Y \leq_p X$ . If  $X$  can be solved in polynomial time, then so can  $Y$ .

Take the contrapositive!

[ Spps  $Y \leq_p X$ . If  $Y$  cannot be solved in polynomial time, then  $X$  can't be solved in polynomial time.

So if we take a "hard" problem & reduce it to another problem X, then X must be at least as hard.

Useful!

If we want to show a problem is NP-Hard, reduce a known NP-Hard problem to it!

↑↑↑  
Important!!

Ex: SAT

no  $\forall, \exists$

→ Input: boolean formula

Q: Can we assign boolean values to the variables so that the formula evaluates to true?

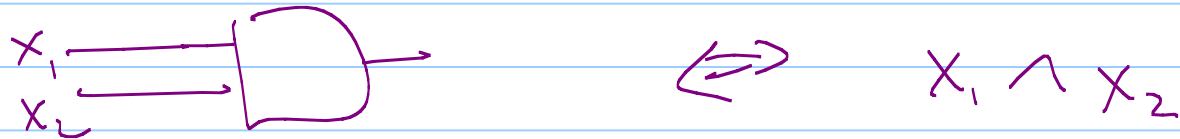
Ex:

→  $(a \vee b \vee c \vee \bar{d}) \Leftrightarrow ((b \wedge \bar{c}) \vee (\bar{a} \Rightarrow d) \vee (c \neq a \wedge b)),$

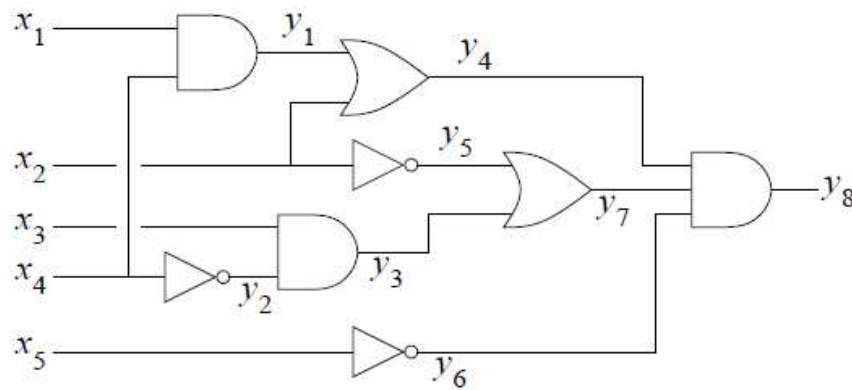
Show SAT is NP-Hard.

How to show NP-Hard?

Reduce circuit SAT to SAT



Ex:



$$(y_1 = x_1 \wedge x_4) \wedge (y_2 = \overline{x_4}) \wedge (y_3 = x_3 \wedge y_2) \wedge (y_4 = y_1 \vee x_2) \wedge \\ (y_5 = \overline{x_2}) \wedge (y_6 = \overline{x_5}) \wedge (y_7 = y_3 \vee y_5) \wedge (y_8 = y_4 \wedge y_7 \wedge y_6) \wedge y_8$$

Alg: BFS through circuit

So in  $O(n)$ , I can transform circuit into a boolean formula.

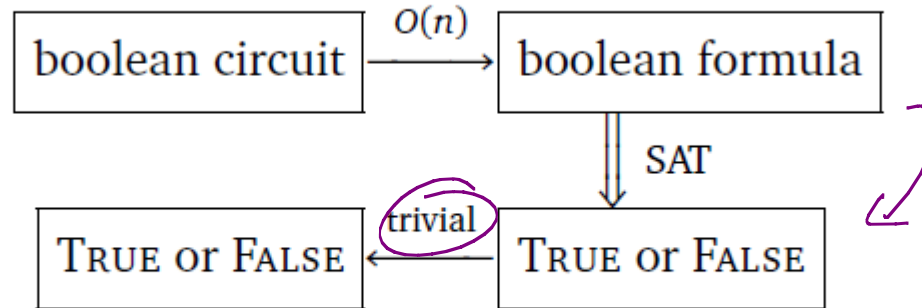
But careful! Need conversion to be polynomial time, & size to stay polynomial.

Well, said takes  $O(n)$  to transform.

Every gate gives us 1 clause in the output formula.

# Reduction Picture

Circuit SAT  $\leq_p$  SAT



$$T_{\text{CSAT}}(n) \leq O(n) + T_{\text{SAT}}(O(n)) \implies T_{\text{SAT}}(n) \geq T_{\text{CSAT}}(\Omega(n)) - O(n)$$

Circuit is satisfiable  $\Leftrightarrow$  boolean formula is satisfiable



## Another example: 3SAT

Dfn: A boolean formula is in conjunctive normal form (CNF) if it is a conjunction (or AND) of clauses, each of which is a disjunction (or OR) of variables or negations.

Ex:  $(a \vee b \vee c \vee d) \wedge (b \vee \bar{c} \vee d) \wedge (\bar{a} \vee b)$

↑  
clause of  
variables  
or-ed  
together

↑  
"and" the  
clauses together

3SAT (cont)

$$(a \vee b \vee c) \wedge (\bar{d} \vee \bar{a} \vee b) \wedge \dots$$

exactly 3 variables in each clause

Def: A 3CNF formula has exactly 3 variables per clause.

3SAT: Given a 3CNF formula, is there an assignment of variables which makes the formula evaluate to true?

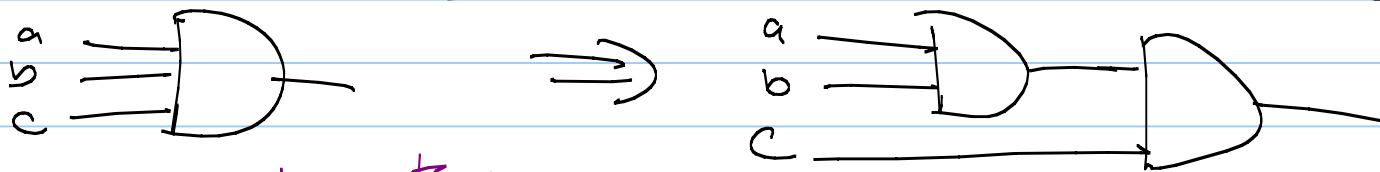
How do we show NP-Complete?

Reduce

Circuit SAT or SAT to 3SAT.

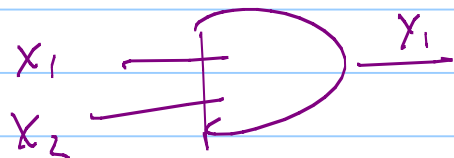
Reduction from Circuit SAT:

① Make every and/or gate have only 2 inputs.



(replace with <sup>k inputs</sup> binary tree of  $k-1$  2input gates)

② Write down clause for each gate  
(same as before)



$$(y_1 = x_1 \wedge x_2)$$

not  $\uparrow$  CNF

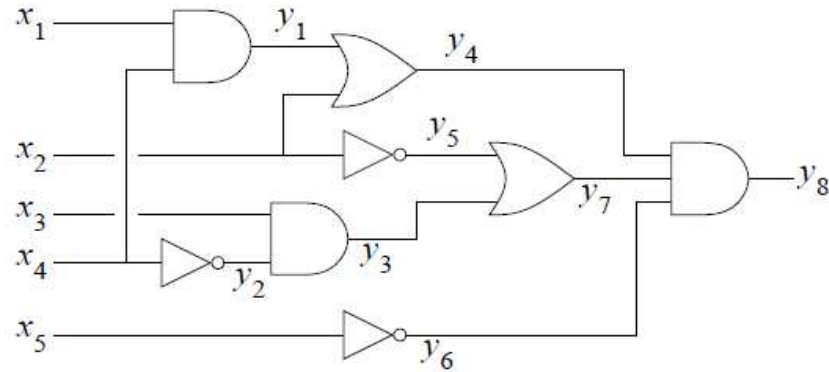
③ Change each clause to CNF formula:

$$\begin{aligned} a = b \wedge c &\quad \longmapsto (a \vee \bar{b} \vee \bar{c}) \wedge (\bar{a} \vee b) \wedge (\bar{a} \vee c) \\ a = b \vee c &\quad \longmapsto (\bar{a} \vee b \vee c) \wedge (a \vee \bar{b}) \wedge (a \vee \bar{c}) \\ a = b &\quad \longmapsto (a \vee b) \wedge (\bar{a} \vee \bar{b}) \end{aligned}$$

④ Make sure every clause has 3 literals:

$$\begin{aligned} a &\quad \longmapsto (a \vee x \vee y) \wedge (a \vee \bar{x} \vee y) \wedge (a \vee x \vee \bar{y}) \wedge (a \vee \bar{x} \vee \bar{y}) \\ a \vee b &\quad \longmapsto (a \vee b \vee x) \wedge (a \vee b \vee \bar{x}) \end{aligned}$$

Ex:



$\Rightarrow$

$$\begin{aligned} & (y_1 \vee \bar{x}_1 \vee \bar{x}_4) \wedge (\bar{y}_1 \vee x_1 \vee z_1) \wedge (\bar{y}_1 \vee x_1 \vee \bar{z}_1) \wedge (\bar{y}_1 \vee x_4 \vee z_2) \wedge (\bar{y}_1 \vee x_4 \vee \bar{z}_2) \\ & \wedge (y_2 \vee x_4 \vee z_3) \wedge (y_2 \vee x_4 \vee \bar{z}_3) \wedge (\bar{y}_2 \vee \bar{x}_4 \vee z_4) \wedge (\bar{y}_2 \vee \bar{x}_4 \vee \bar{z}_4) \\ & \wedge (y_3 \vee \bar{x}_3 \vee \bar{y}_2) \wedge (\bar{y}_3 \vee x_3 \vee z_5) \wedge (\bar{y}_3 \vee x_3 \vee \bar{z}_5) \wedge (\bar{y}_3 \vee y_2 \vee z_6) \wedge (\bar{y}_3 \vee y_2 \vee \bar{z}_6) \\ & \wedge (\bar{y}_4 \vee y_1 \vee x_2) \wedge (y_4 \vee \bar{x}_2 \vee z_7) \wedge (y_4 \vee \bar{x}_2 \vee \bar{z}_7) \wedge (y_4 \vee \bar{y}_1 \vee z_8) \wedge (y_4 \vee \bar{y}_1 \vee \bar{z}_8) \\ & \wedge (y_5 \vee x_2 \vee z_9) \wedge (y_5 \vee x_2 \vee \bar{z}_9) \wedge (\bar{y}_5 \vee \bar{x}_2 \vee z_{10}) \wedge (\bar{y}_5 \vee \bar{x}_2 \vee \bar{z}_{10}) \\ & \wedge (y_6 \vee x_5 \vee z_{11}) \wedge (y_6 \vee x_5 \vee \bar{z}_{11}) \wedge (\bar{y}_6 \vee \bar{x}_5 \vee z_{12}) \wedge (\bar{y}_6 \vee \bar{x}_5 \vee \bar{z}_{12}) \\ & \wedge (\bar{y}_7 \vee y_3 \vee y_5) \wedge (y_7 \vee \bar{y}_3 \vee z_{13}) \wedge (y_7 \vee \bar{y}_3 \vee \bar{z}_{13}) \wedge (y_7 \vee \bar{y}_5 \vee z_{14}) \wedge (y_7 \vee \bar{y}_5 \vee \bar{z}_{14}) \\ & \wedge (y_8 \vee \bar{y}_4 \vee \bar{y}_7) \wedge (\bar{y}_8 \vee y_4 \vee z_{15}) \wedge (\bar{y}_8 \vee y_4 \vee \bar{z}_{15}) \wedge (\bar{y}_8 \vee y_7 \vee z_{16}) \wedge (\bar{y}_8 \vee y_7 \vee \bar{z}_{16}) \\ & \wedge (y_9 \vee \bar{y}_8 \vee \bar{y}_6) \wedge (\bar{y}_9 \vee y_8 \vee z_{17}) \wedge (\bar{y}_9 \vee y_8 \vee \bar{z}_{17}) \wedge (\bar{y}_9 \vee y_6 \vee z_{18}) \wedge (\bar{y}_9 \vee y_6 \vee \bar{z}_{18}) \\ & \wedge (y_9 \vee z_{19} \vee z_{20}) \wedge (y_9 \vee \bar{z}_{19} \vee z_{20}) \wedge (y_9 \vee z_{19} \vee \bar{z}_{20}) \wedge (y_9 \vee \bar{z}_{19} \vee \bar{z}_{20}) \end{aligned}$$

Looks huge!

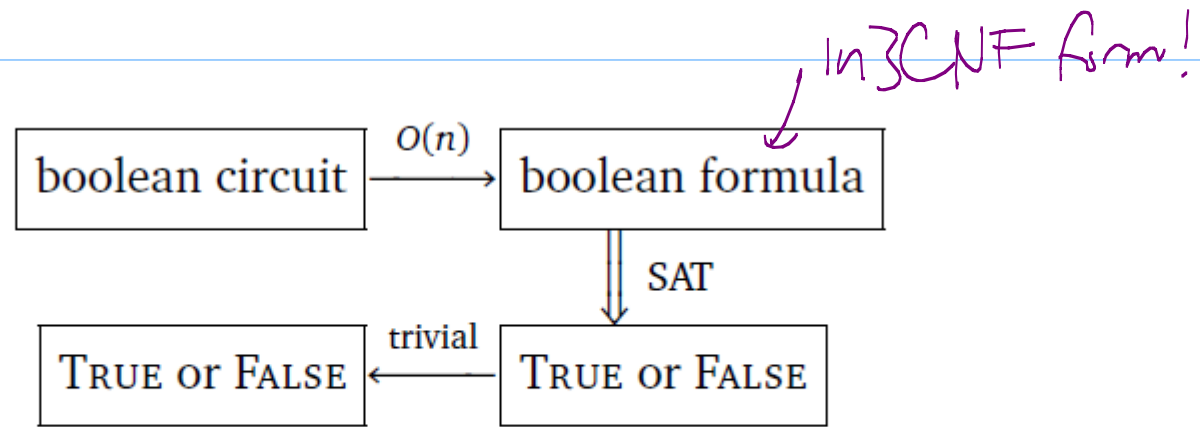
But: • every gate became at most  $(k-1)$  gates,  
where  $k = \#$  of inputs

- Every gate then formed at most 5 clauses

So transformation + size are both polynomial.

$O(n)$  algorithm +  
 $O(n)$  size boolean in 3CNF form.

Recap:



$$T_{\text{CSAT}}(n) \leq O(n) + T_{\text{SAT}}(O(n)) \implies T_{\text{SAT}}(n) \geq T_{\text{CSAT}}(\Omega(n)) - O(n)$$

Circuit is satisfiable  $\Leftrightarrow$  3CNF formula  
is satisfiable

So 3SAT is NP-Complete.

Next time: non-logic reductions  
(I promise!)